JST131N2-800E 1A TRIAC

Rev.A.1.1

D

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	V_D=12V R_L=33Ω	- -	MAX.	10	mA
				25	
V_{GT}		ALL	MAX.	1.3	V
V_{GD}	V_D=V_{DRM} T_j=125 R_L=3.3kΩ	ALL	MIN.	0.2	V
I_L	I_G=1.2I_{GT}	- -	MAX.	15	mA
				30	
I_H	I_T=50mA		MAX.	15	mA
dV/dt	V_D=540V Gate Open T_j=110		MIN.	200	V/μs
(dV/dt)c	(dI/dt)c=0.44A/ms, T_j=110		MIN.	6	V/μs
t_{on}	I_G=40mA I_A=200mA I_R				

FIG.1: M on-state current versus case temperature

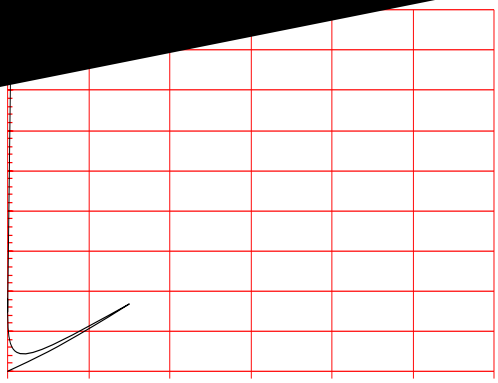
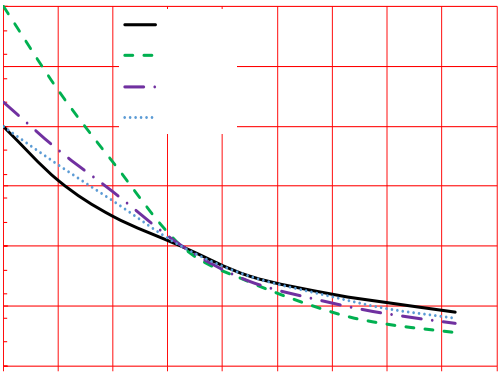


FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature



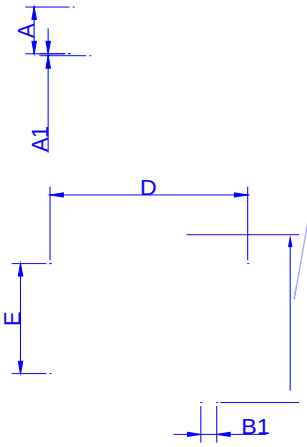
ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		- -				
JST131N2-800E	800	10	25	SOT-89-2L	4,000	Tape & Reel

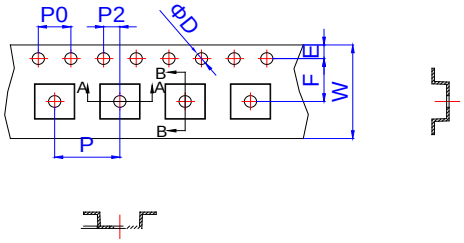
Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Oct.16, 2025		

PACKAGE MECHANICAL DATA



DELIVERY MODE



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
E	1.65	1.75	1.85	0.065	0.069	0.073
F	5.45	5.50	5.55	0.215	0.217	0.219
P2	1.90	2.00	2.10	0.075	0.079	0.082
D	-	1.50	1.60	-	0.059	0.063
D1	1.50			0.059		
P0	3.90	4.00	4.10	0.154	0.157	0.161
10P0	39.80	40.00	40.20	1.567	1.575	1.583
W			12.30			0.482
P	7.90	8.00	8.10	0.311	0.315	0.319
A0	5.20	5.30	5.40	0.204	0.208	0.212
B0	4.80	4.90	5.00	0.188	0.192	0.196
K0	1.75	1.85	1.95	0.069	0.073	0.076
t	0.20	0.25	0.30	0.008	0.010	0.012
θ	3°		5°	3°		5°