



JST136K-800E 4A TRIAC

(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I _{GT}	V _D =12V R _L =33	- -	MAX.	10	mA
				25	
V _{GT}		ALL	MAX.	1	V
V _{GD}	V _D =V _{DRM} T _j =125 R _L =3.3k	ALL	MIN.	0.2	V
I _L	I _G =1.2I _{GT}	-	MAX.	20	mA
		-		30	
I _H	I _T =100mA		MAX.	25	mA
dV/dt	V _D =540V Gate Open T _j =110		MIN.	250	V s
(dV/dt) _c	(dI/dt) _c =1.8A/ms, T _j =110		MIN.	6	
t _{on}	I _G =40mA I _A =200mA I _R =20mA T _j =25		TYP.	1.5	s
t _{off}				15	

Symbol	Parameter		Value(MAX.)	Unit
V _{TM}	I _{TM} =5A t _p =380 s	T _j =25	1.7	V
V _{TO}	Threshold voltage	T _j =125	0.94	V

R_D Dynamic resistance T_j=125 D_{VF}=25 EMC ET 5n <</MO

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-800

E

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Blank:Tube
-TR:Tape & Reel

E:IGT1-3 10mA IGT4 25mA

800:VDRM /VRRM 800V

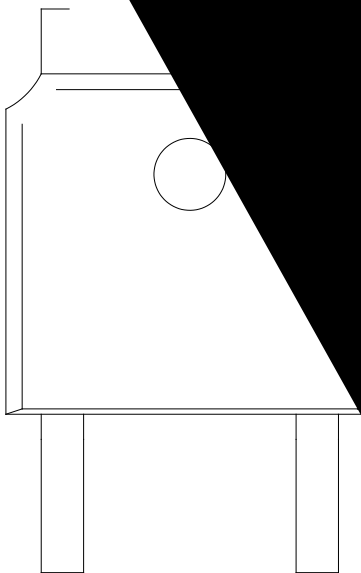


FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

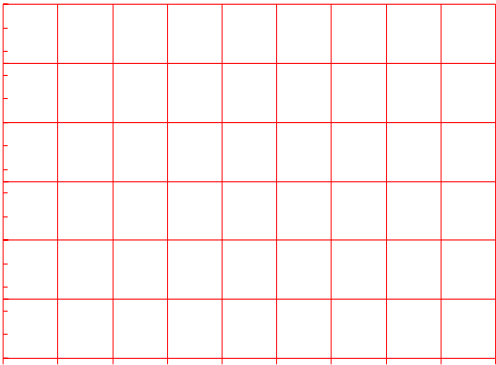
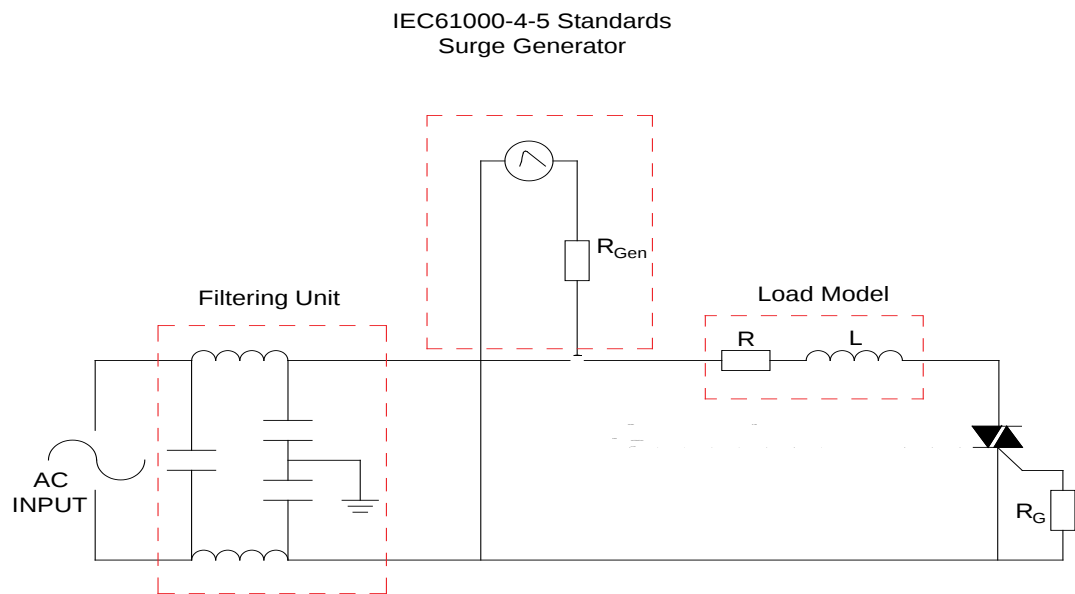
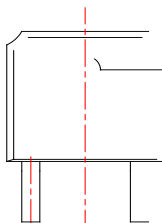


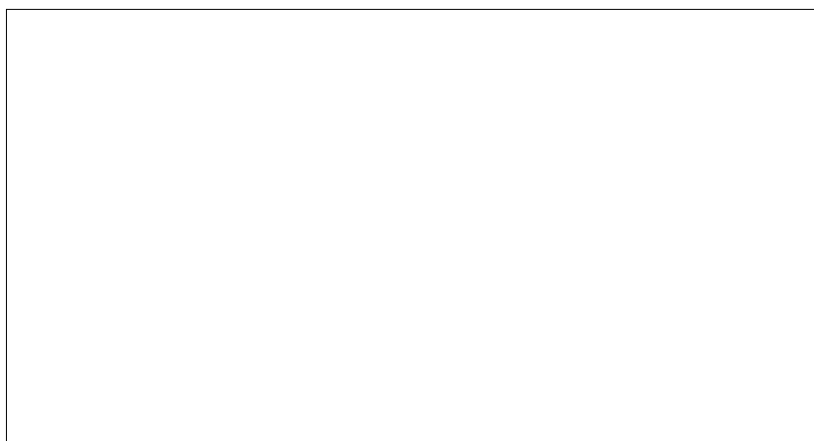
FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Order code	Voltage V_{DRM}
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Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.15	0		0.006
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1						
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
G1	2.18		2.38	0.086		0.094
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065



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